K a n n a l e   S a t i s h   K u m a r

BKIT,Bhalki
Dist:Bidar,
Karnataka
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About myself:

- My objective is to exercise my knowledge and experience to improve the status of the institution I serve among others and to prove myself as an asset to the institution.

- Presently working as Associate   professor(J) in Bhemanna   Khandre Institute of Technology, Bhalkl

Teaching Experience:

Sl No	Designation	Department	Institution	Duration	Period
1	Associate Professor(J)	Electronics and Communication Engineering	Bhemanna Khandre Institute of Technology, Bhalki	12^+	Sep-2007 to till date
2	Sr.Lecturer	Electronics and Communication Engineering	Bhopal Institute of Technology & Science, Bhopal, M.P.	3year 2Months	Since June 2004 to Aug-2007
3	Lecturer	Electronics and Communication Engineering	Anurag Engineering College, Kodad Andhra Pradesh	2year	May 2002 to May 2004

Industrial Experience:

Sl No	Designation	Department	Organization	Duration	Period
1	Apprentice Trainee	Integrated Circuit Design Center	Bharat Electronics Limited, Bangalore, Karnataka	1 year	October 2000 to October 2001

Academic Chronicle:

Course	University	Year of passing	College	Score
Ph. D	VTU Belagavi	Pursuing	VTU	
M. Tech (Micro Wave and Millimeter Wave)	Maulana Azad N.I.T, Bhopal (M.P) (Deemed University)	Aug-2007	Maulana Azad N.I.T, Bhopal (M.P)	7.39 (C.G.P.A)
B.E. (Electronics and Instrumentation)	Gulbarga University, Gulbarga, Karnataka	Nov-1999	Poojya Doddappa Appa College of Engineering, Gulbarga, Karnataka	77.3%

Subject Taught

1)   Basic Electronics
2)   Analog Electronic circuit
3)   Linear ICs and applications
4)   Electronics Instrumentation
5)   Microwaves and Rada
6)   Wireless communication

Publications

International Journal:-

- **Satish Kannale**, Dr. Kavita Khare, Deepak A, Mallikarjun M, "FPGA Implementation of Selective Huffman coding for high speed data compression and decompression" ,World Journal of science and Technology, Vol. 1 No. 8,2011,pp 89-93
- **Satish Kannale** Enhanced LEACH Multipath Based Energy Efficient Routing for Wireless Sensor Network.
- **Satish Kannale, Ramesh D. Dr. G.S.Biradar "Energy efficient algorithm and simulator for wireless sensor Network" IJCAE vol.4 Issue-3 September 2013 Page No. 437 to 449.**

International and National Conference:-

1. Satish Kannale, Dr. Kavita Khare, Deepak A, Mallikarjun M, "FPGA Implementation of Selective Huffman coding for high speed data compression and decompression" , Oct.29-30,2010, ICN2010,REC Bhalki, pp.409-412.

2. Satish Kannale, Ramesh D, & Kalpana C." Design of ARQ and Hyrid ARQ protocol for wireless channel using BCH codes RITS ICAEM 2013 Page-665 to 668.
3. Satish Kannale " Fiber optic based Hybrid devices for simultaneous measurement humidity and temperature" NCST-2014, BKIT Bhalki Pages from 682 to 684.
4. Satish Kannale " Design and analysis of rectangular microstrip Patch Antenna using Meta Material for improved efficiency" 16-11-2014 ISCA-ISC-2014

Workshops / Seminars/ Short-term Courses:-

1. Attended 2-days National level Industry Institute Interaction Programme on VLSI design and Embedeed System from 27-28 Sept.2013 atBKIT Bhalki
2. Attended 3-days summer school advances in photonic networks and communication engineering (APCE-2013) From June 14-16, 2013 at IIT Bhubaneshwar.
3. Attended 5-days winter School on recent innovations in HDL Microprocessors" from January 21-25 2014, at BKIT Bhalki.
4. Attended 2-days National level work shop on Advances in Communication (ACOM-2014) from 4-5 April 2014.

5. Attended 2-days National level work shop on Microwave,Antenna and Propagation for Modern wireless Communication (WMAP-2014) on 7-8 February 2014
6. Attended two days National Workshop on "Recent Advances in communication"

on 06th & 07 th August, 2010, at REC Bhalki

7. Attended "Faculty development programme on Internalize intellectual property & strategic management of intellectual property Rights" organized by VTU Entrepreneurship development cell, on 13th march 2012, at REC Bhalki

8. Attended Wipro Mission 10X workshop sponsored by ISTE from 18th to 22th March 2012, at REC Bhalki

9. Attended workshop on "High Impact Teaching Skills" Attested by Dale Carnegie & Associates,Inc.Trainer & Wipro on 19th & 20th March 2012, at REC Bhalki

10. Attended one day workshop on "MSP-430 Ultra low power microcontroller" on 4th February 2012, at REC Bhalki

11. Attended National level seminar on "Recent trends in cloud computing 0n 2nd January 2012, at Karnataka arts, science & commerce college , Bidar

12. Attended National level seminar on "Recent trends in Wirless networks" on 30th October to 31stOctober 2009, at Guru Nanak Dev Engg. College , Bidar

13. Attended one day workshop on "Fundamentals of HDL" on 21st February 2009 at REC Bhalki

14. Attended workshop on "Teaching learning Quality improvement initiative" from 01st to 04th February 2009, at REC Bhalki

15. Attended one day workshop on "MATLAB & SIMULINK" 0n 30th December 2008 at REC Bhalki

16. Attended one day workshop on "QIP" 0n 30th December 2008 at REC Bhalki

17. Attended workshop on "VLSI design using CAD tool" on 27th & 28th October 2007, at Hospet

18. Under gone " VLSI Technology Training Course" Sponsored by AICTE, at Centre for Research and Industrial Staff Performance, Bhopal (MP)

19. Attended ISTE and IETE Sponsored 6 Days National Level Faculty Development Program on " E – Resources for Academic Excellence " from 8th to 13th July 2019 at GNDEC, Bidar

20. Attended TEQIP 1.3 Sponsored Five Days Faculty Development Program on " Computational Methods for Big Data Analytics " from 6th to 10th Jan 2020 at VTU Campus ,Belagavi

21, Attended one week AICTE – VTU Joint Training Programme for Teachers on " A Overview of
    Teacching Techniques in Wireless Cellur & LTE 4G Broad Band " from 27th to 31 th Jan-2020 at
    VTU-HRDC , Center for PG Studies, Muddenahalli, Chickkaballapur.

22. Attended Five Days Virtual Faculty Development Program on " Next Generation Electronics and Computer Networks " from June 2nd to June 6th at BKIT , Bhalki

.Members of Various Bodies:

1. IETE Membership: as a Fellow.

Administration:

1 Worked as a Deputy Chief Superintendent (External) , VTU Examination

2 Worked as a Squad Member , VTU Examination

3 Worked as a Special Squad Member, VTU Examination

4 Worked as a Squad Chairman, VTU Examination

Personal Profile:

Date of birth	:	1st June 1975
Father	:	Devendrappa
Languages Known	:	English, Hindi, Kannada
Permanent Address	:	Dongapur Post, Bhalki taluk, Bidar District, Karnataka

Kannale Satishkumar